

(11)Publication number:

06-348234

(43)Date of publication of application: 22.12.1994

(51)Int.CI.

G09G 3/36 G02F 1/133

(21)Application number : 05-157846

(22)Date of filing:

02.06.1993

(71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

(72)Inventor: FUJIWARA SHOZO

FURUBAYASHI YOSHINORI

WAKITA HISAHIDE

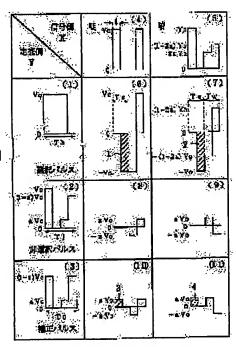
(54) METHOD FOR DRIVING DISPLAY PANEL

(57)Abstract:

PURPOSE: To eliminate the flicker at the time of erasing display data of

the liquid crystal display panel.

CONSTITUTION: The selection period Th of the respective scanning electrodes of the display panel of a characteristics to exhibit two stable pulses is composed of an erasing period Te and a writing period Tw. Erasing pulses of monopolarity are impressed to reset all the pixels on the selected scanning electrodes to the dark in the erasing period Te. All the pixels on the selected scanning electrodes are changed to the bright or the dark according to the display data to display video signals in the writing period Tw. The DC components of the voltage pulses remain in the selection period Th at this time and, therefore, correction pulses are formed and are impressed in a voltage correction period Tp in order to offset these components. The flicker is then eliminated and since the DC components of the voltage pulses do not remain, the deterioration in the characteristics of the liquid crystal cell is prevented.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application

converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of

rejection]

[Date of requesting appeal against examiner's decision

of rejection]

[Date of extinction of right]

BEST AVAILABLE COPY

Copyright (C); 1998,2003 Japan Patent Office